

Amendments to the Specification

Please amend the specification as follows:

(New paragraph between paragraphs 21 and 22):

[21-a] Fig. 8 is a block diagram of a general purpose computer system for practicing the present invention.

[p31] Where the parameters C1, C2 and Rload are constants, provided by the caller. V2 in the above equation ~~starts~~ is equal to Vout at the beginning of simulation.

[p38] “[...] In each instance of a selection of pin voltages, ~~The user~~ the user measures the steady state current flowing into/out of the pins. [...]”

[p46] “[...]Whatever implementation of Zint is chosen, the current flowing into Zint, namely I(Zint), can change continuously as a function of time, and may therefore ~~different~~ have a different value at every time point. [...]”

~~[p622]~~ [p62] The third step is to take ~~make~~ measurements [...]

[p79] “[...] In the case of the simulator’s API, [...]”

[87] “[...] The interface call prototype would reflect the choice of load-modeling as the load is an input into the ~~circuit module~~ circuit model. [...]”

~~[p975]~~~~[p95]~~ // Power level B:

Amendments to the Drawings

Applicants have amended the label of Figure 7 element 25 to read “code module 25” as opposed to “circuit module 25”.

A replacement sheet for Figure 7 is attached hereto in Appendix A in accordance with 37 CFR 1.121(d).

Figures 1-8 have been amended to darken the lines and overcome the draftsmen’s objection to figures 1-8 because they were too light and difficult to see. Replacement sheets for figures 1-8 are attached hereto in Appendix A in accordance with 37 CFR 1.84.